# SWA Review Document

## Summary

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| --- | --- |
| **Date** | September 25 2021 |
| **Effort** | 1.5 hours |
| **Room/Location** | Virtual |
| **Review Status** | Open |
| **Review name** | SWA\_Act3.2\_MaresMejia\_MarcoAntonio.doc |
| **Method** | WT |
| **Release** | 1.0.0 |
| **Responsible** | Ariel Antonio Gonzalez Padilla |
| **Project** | Door Control module |
| **Reason of Review** | First Revision |

## Comment List

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| --- | --- | --- | --- | --- | --- |
| **No.** | **Reference** | **Comments / Actions** | **Classification (E)rror/Risk / (R)emark** | **Responsible person/Planned date for completion** | **Completion(Name/Date)** |
| 1 | Definitions and abbreviations | This section explains some of the more complex terms of the architecture, but some complementary definitions may be added to make the document more accessible | Remark | Marco Mares | 15/9/21 |
| 2 | Table of functions – Anti Pinch Event | The writing in this function suggests a complex implementation that could be problematic | Risk | Marco Mares | 15/9/21 |
| 3 | Table of functions - HW Diagnosis | This function should be separated to avoid complexity, in the requirements are written at least 3 different types of diagnostics which use different types of communication | Error | Marco Mares | 15/9/21 |
| 4 | Functional interfaces | The interfaces in the functional section could be more general and modular | Remark | Marco Mares | 15/9/21 |
| 5 | Functional interfaces – HW variant | The hardware variants in this project may be out of the software scope. The details that make every configuration unique in the software are necessary, yet all the hardware variants stated on the requirements may be extra information. | Error | Marco Mares | 15/9/21 |
| 6 | Functional Interaction | In this sequence diagram are not always marked the information replays that are shown on the block diagram, this could lead to confusion. | Risk | Marco Mares | 15/9/21 |
| 7 | physical requirements – Manufacturing info | It may be complex to set the SW and HW variants on the same component maintaining an acceptable level of consistency | Risk | Marco Mares | 15/9/21 |
| 8 | Physical interfaces | The sequence diagram requires more work to reach maturity and represent the complete process | Remark | Marco Mares | 15/9/21 |
| 9 | Integration Plan | Having the integration step “Read CAN frame” third in the integration plan could lead to problems due to an incomplete framework | Risk | Marco Mares | 15/9/21 |
| 10 | SW requirements allocation | Several requirements in the traceability matrix don’t have a SW requirement assigned, a revision to these statements is needed | Error | Marco Mares | 15/9/21 |

## Check List

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| --- | --- | --- | --- | --- | --- |
| No | Description | OK / NOK / NR | Comment | Responsible person /  Planned date for completion | Status |
| 1 | Does the design comply to the SW Requirements? | OK |  |  | Closed |
| 2 | Are all requirements allocated to Architectural elements? | NOK | Just some adjustments are needed to eliminate redundancy | Marco Mares - 8/9/21 | Open |
| 3 | Is the system context (environment) of the software architecture described? | NR | No environment is given on the project guide |  | Closed |
| 4 | Is a global overview of all SW function blocks (Functional Architecture) and SW components (Physical Architecture) provided, giving the reader a good understanding of the overall dependencies? | OK |  |  | Closed |
| 5 | Are the functions identified from SW requirements and reviewed there are no missing functions? | OK |  |  | Closed |
| 6 | Are the functional interfaces documented? | OK |  |  | Closed |
| 7 | Is each SW function mapped to one or more SW components? I.e. is there a mapping from functional to physical software architecture? | OK |  |  | Closed |
| 8 | Do SW components' interfaces export only the needed data and functions (encapsulation principle)? If not, are the reasons given? | OK |  |  | Closed |
| 9 | Are diagrams (e.g. use case charts, sequence charts…) used were appropriate? | OK | Yes, but some may need more work | Marco Mares 8/9/21 | Open |
| 10 | Are the SW components identified? | OK |  |  | Closed |
| 11 | Are the Physical Interfaces identified with a clear usage description? | NOR | Some may need further clarification | Marco Mares - 8/9/21 | Open |
| 12 | Are the OS task properly defined and documented? | OK |  |  | Open |
| 13 | Are the Interrupt usage described? | OK |  |  |  |
| 14 | Are there Power Modes identified and documented? | NR |  |  | Closed |
| 15 | Is the integration Plan properly described with the corresponding order for the software construction order? | OK |  |  | Closed |
| 16 | Is the functional safety information properly described on the SWA document? | NR | This section is field in module 4 | Marco Mares - 8/12/21 | Open |